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Department of Electronics and Communication Engineering

Department of Electronics and Communication Engineering
Francis Xavier Engineering College, Vannarpettai, Tirunelveli - 627 003
(An Autonomous Institution - Affiliated to Anna University, Chennai)
www.francisxavier.ac.in

9th Board of Studies Meeting in the Department of Electronics & Communication Engineering

Mode: Through Offline and Online mode (Meeting Link: <https://meet.google.com/pjy-cyjo-wyb>)

Venue: FXEC, Presentation hall

Date & Time: 13th June, 2023 at 11.00 a.m

AGENDA

BoS / ECE 9.1: Welcome address and Opening Remarks by Chairman, Board of studies in the Department of ECE

BoS / ECE 9.2: Confirmation of the Minutes of the 8th meeting of Board of studies in the Department of Electronics & Communication Engineering held on 28th February, 2023.

BoS / ECE 9.3: Business brought forward by the Chairman, Board of Studies, which is

9.3.1 Syllabi of Semesters VII and VIII for UG - B.E Electronics & Communications Engineering - for Regulations 2021

9.3.2 R2021(Amendment 2023) B.E-ECE Curriculum and Syllabi

9.3.3 R2021-Department specific Value Added Skill

BoS / ECE 9.4: Other suggestions given by the BOS Members

Department of Electronics & Communication Engineering
MINUTES OF THE MEETING

The 9th Meeting of the Board of studies of the Electronics & Communication Engineering was held on 13th June, 2023 at 11.00 a.m through both offline & online.
The following members were present:

S. No	Members as per UGC Norms	Members Nominated
1	Head of the Department concerned (Chairman)	Mrs.J.Friska, Head & Asso. Prof, ECE department
2	One Expert nominated by the Vice Chancellor from a panel of six recommended by the college Principal	Dr. S. Baulkani, Professor, Department of Electronics and Communication Engineering, Government College of Engineering, Tirunelveli
3	Two experts in the subject from outside the college nominated by the Academic Council	Dr.S.Anand Associate Professor Department of Communication Engineering, School Of Electronics Engineering (Sense)VIT-Vellore
		Dr.P.Sivakumar Professor, Department of Electronics and Communication Engineering, Kalasalingam University, Krishnankoil-626126. Virudhunagar, Tamil Nadu, India.
4	One representative from industry/corporate sector/allied area relating to placement	Er. Francy Williams Senior Engineer, All India Radio , Palayamkottai, Tirunelveli
5	One Post Graduate meritorious aluminous nominated by the Principal	Er. S.J. Jinolin Jacob HR manager, US Operations Insigma Technologies Pvt Ltd Trivandrum

Internal Members Present:

S. No	Name of the Member	Designation
1	Mr.C.Amarsingh Feroz	Asso. Prof./ECE
2	Dr.R.Kabilan	Asso. Prof/ECE
3	Dr.P.Kannan	Asso. Prof/ECE
4	Dr.K.Lakshmi Narayanan	Asso. Prof/ECE

5	Dr.R.PremAnanth	Asso. Prof/ECE
6	Dr.Andrew Roobert	Asso. Prof/ECE
7	Dr.M.Suresh Chinna Thampy	Asst. Prof/ECE
8	Dr.S.Allwin Devaraj	Asst. Prof/ECE
9	Dr. M.V.Rajee	Asst. Prof/ECE
10	Mrs.M.Chitra Evangelin Christina	Asst. Prof/ECE
11	Mrs.E.Francy Irudaya Rani	Asst. Prof/ECE
12	Mr.B.Pradheep T Rajan	Asst. Prof/ECE
13	Mrs.M.Radha	Asst. Prof/ECE
14	Mrs.T.Ramya	Asst. Prof/ECE
15	Mrs.S.Balammal@Geetha	Asst. Prof/ECE
16	Mrs.P.Bhuvana	Asst. Prof/ECE
17	Ms.S.Sherlin Leela Princy	Asst. Prof/ECE
18	Mrs.U.Lenin Marksia	Asst. Prof/ECE

Members Absent:

Nil

Board of Studies Meeting started with a prayer by Mr.C.Amarsingh Feroz, Asso.Prof/ECE

BOS / ECE 9.1: WELCOME ADDRESS AND OPENING REMARKS BY CHAIRMAN, BOARD OF STUDIES IN THE DEPARTMENT OF ECE

The BoS member Secretary Dr.S.Allwin Devaraj, AP/ECE welcomed and introduced the Members and Special Invitees of Board of Studies and Management dignitaries by depicting their respective academic and professional profile.

The Chairman, BOS of Electronics & Communication Engineering once again welcomed the members of 9th Board of Studies and thanked each of them for sparing their valuable time to attend the meeting.

BOS / ECE 9.2: CONFIRMATION OF THE MINUTES OF THE 8th MEETING OF BOARD OF STUDIES IN THE DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING HELD ON 28th February, 2023 AND ACTION TAKEN REPORT OF 8th MEETING OF BOARD OF STUDIES

The minutes of the Eighth Board of Studies meeting held on 28th February, 2023 were communicated to the members. The comments received have been incorporated and placed for confirmation. The same was approved by the 8th Academic Council Meeting.

➤ *Action taken report (Enclosed in Annexure I)*

BOS / ECE 9.3: BUSINESS BROUGHT FORWARD BY THE CHAIRMAN, BOARD OF STUDIES:

9.3.1 SYLLABI OF SEMESTERS VII & VIII FOR UG - B.E ELECTRONICS & COMMUNICATIONS ENGINEERING - FOR REGULATIONS 2021

The chairman presented the newly framed 2021 regulation syllabi for Semester VII to VIII.

- For the benefits of the students, it was suggested that Microcontroller Lab can focus towards **Embedded C**.
 - It is suggested to introduce **FPGA for Embedded System Applications** instead of ASIC & FPGA Design and **TCAD** instead of CAD.
 - **Lower, Moderate and High level Questions** can be set for every topic, so that student's progress can be easily tracked.
 - More number of Elective Courses related to **IOT** was incorporated in the syllabus, which can be reduced.
 - **21EC8911 - Project Work II/Startup** has short time span. Many factors like Outsiders, Registration, etc are involved. For this, Progress has to be monitored periodically. Startups should not involve in just marketing, rather Technical knowledge should be incorporated for societal benefit. Students should be able to develop own product and should commercialize it.
 - **Total Credits** has been changed from **166 to 168** from 2023 -2024 onwards. This is because of the inclusion of Tamil courses (1 credit each in I and II semester).Also Indian constitution, a mandatory zero- credit course has been removed from the Curriculum.
- **RESOLVED TO APPROVE the above-mentioned suggestions for Curriculum and syllabi of Semester 7 & 8 - B.E Electronics & Communication Engineering**

9.3.2: R2021 (AMENDMENT 2023) B.E - ECE CURRICULUM AND SYLLABI

- 21HS2103/Technology in Tamil Culture, a 1- credit paper was introduced newly. A Transdisciplinary curriculum that offers courses from Arts and Sciences Colleges under open elective category was suggested.

➤ **RESOLVED TO APPROVE Curriculum and syllabi of R2021(Amendment 2023)**

9.3.3: R2021-DEPARTMENT SPECIFIC VALUE ADDED SKILL

- 21EC2DS01/PCB design is a pedagogical design, which can be incorporated into final year project, so that it will be more fruitful.

- List of Department Specific Value Added Skill for Approval (Enclosed in Annexure II)

➤ **RESOLVED TO APPROVE the Department Specific Value Added Skill.**


BoS / ECE 9.4: OTHER SUGGESTIONS GIVEN BY THE BOS MEMBERS

- ✓ Technical training programs can be offered to the students by holding hands with industries and also knowing trends from recent Passed out students placed in Core companies.
- ✓ The professional core should not be compromised at any cost.

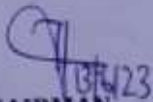
Based on the suggestions given by the members, BOS resolved to recommend the following to the Academic Council for further approval.

- **RESOLVED TO APPROVE the Curriculum for 2 semesters and syllabi of Semester 7 & 8 - B.E Electronics & Communication Engineering.**
- **RESOLVED TO APPROVE Curriculum and syllabi of R2021 (Amendment 2023).**
- **RESOLVED TO APPROVE the Department Specific Value Added Skill - B.E Electronics & Communication Engineering**

The Chairman thanked all the members for their suggestions and substantial inputs provided in the meeting. Finally, the meeting ended with a vote of thanks by Dr.P.Kannan, Asso. Prof/ECE.


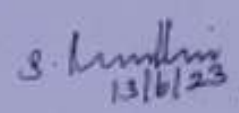
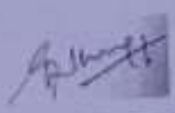




**MEMBER SECRETARY
BOARD OF STUDIES**









**CHAIRMAN
BOARD OF STUDIES**

Attendance Sheet Table of Members

External Members present:

S. No	Members as per UGC Norms	Members Nominated	Signature
1	Head of the Department concerned (Chairman)	Mrs. Priska, Head & Asso. Prof. ECE department	 13/6/2023
2	One Expert nominated by the Vice Chancellor from a panel of six recommended by the college Principal	Dr. S. Haukani, Professor, Department of Electronics and Communication Engineering, Government College of Engineering, Tirunelveli	 13/6/23
3	Two experts in the subject from outside the college nominated by the Academic Council	Dr. S. Anand, Associate Professor Department of Communication Engineering, School of Electronics Engineering (Sense), VIT-Vellore	
		Dr. P. Sivakumar, Professor, Department of Electronics and Communication Engineering, Kalasalingam University, Krishnankoil-626126, Virudhunagar, Tamil Nadu, India.	
4	One representative from industry/corporate sector/allied area relating to placement	Er. Francy Williams, Senior Engineer, All India Radio, Palayamkottai, Tirunelveli	
5	One Post Graduate meritorious aluminous nominated by the Principal	Er. S.J. Jinolin Jacob, HR manager, US Operations Insignia Technologies Pvt Ltd Trivandrum	 13/6/23

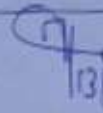
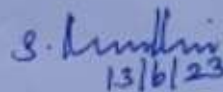

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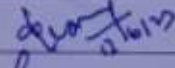

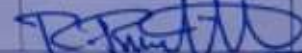
FXEC - Minutes of the 9th BoS Meeting held on 13/06/2023 - Dept of ECE

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Internal Members Present:

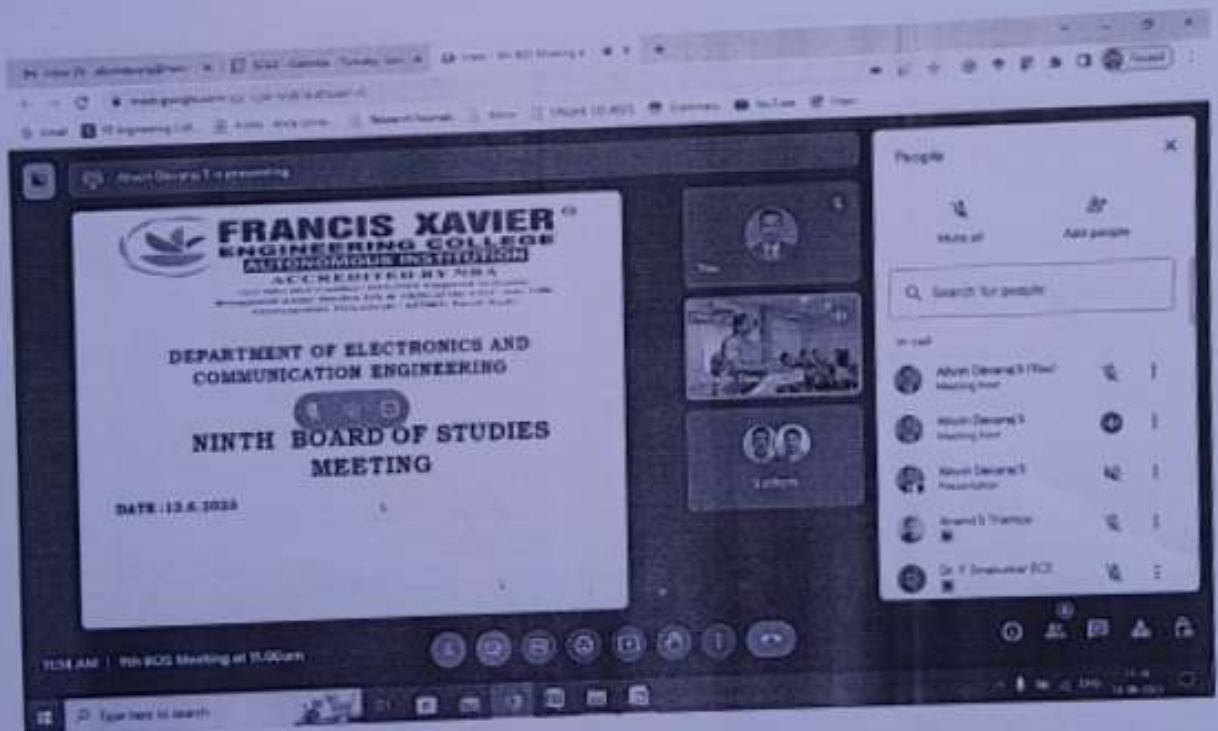
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5	Dr.R.PremAnanth	Asso. Prof/ECE	

6	Dr.Andrew Roobert	Asso. Prof/ECE	<i>A. Andrew</i>
7	Dr.M.Suresh Chinna Thampy	Asst. Prof/ECE	<i>[Signature]</i>
8	Dr.S.Allwin Devaraj	Asst. Prof/ECE	<i>[Signature]</i>
9	Dr. M.V.Rajee	Asst. Prof/ECE	<i>Rajee</i>
10	Mrs.M.Chitra Evangelin Christina	Asst. Prof/ECE	<i>[Signature]</i>
11	Mrs.E.Francy Irudaya Rani	Asst. Prof/ECE	AB
12	Mr.B.Pradheep T Rajan	Asst. Prof/ECE	<i>[Signature]</i>
13	Mrs.M.Radha	Asst. Prof/ECE	<i>[Signature]</i>
14	Mrs.T.Ramya	Asst. Prof/ECE	<i>[Signature]</i>
15	Mrs.S.Balammal@Geetha	Asst. Prof/ECE	<i>[Signature]</i>
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17	Ms.S.Sherlin Leela Princy	Asst. Prof/ECE	<i>[Signature]</i>
18	Mrs.U.Lenin Marksia	Asst. Prof/ECE	<i>[Signature]</i>

Department of Electronics & Communication Engineering on 13th June, 2023 at 11.00 a.m through both Offline and Online mode ((Meeting Link: <https://meet.google.com/pjy-cyjo-wyb>))







The screenshot shows a Zoom meeting interface. The main window displays a presentation slide titled "COURSE GUIDE FOR (UG) SECOND SEMESTER". The slide contains a table with columns for S/N, Course No., Course Name, Credits, and a grid of checkboxes for various semesters (I, II, III, IV). The table lists several courses under "New Courses" and "Valid Courses".

At the bottom of the Zoom window, the status bar shows "11:57 AM | 9th BoS Meeting at 11:00am".

The screenshot shows a Zoom meeting interface. The main window displays a presentation slide with text content. The text includes bullet points and paragraphs, likely detailing meeting minutes or course information. The text is partially obscured by the Zoom interface elements.

At the bottom of the Zoom window, the status bar shows "11:58 AM | 9th BoS Meeting at 11:00am".

Zoom Meeting | 11:28 AM | 9th BGS Meeting at 11:00am

Department Specific Value Added Skills

S.No	Slab Code	Slab Name	Semester	Contact Periods	Credits
1	21EC0001	PLN Design	VI	60	2
2	21EC0002	Mobile Robotics	VI	60	2
3	21EC0003	Signal and Image Processing using MATLAB	VII	60	2
4	21EC0004	Specialized VLSI Design	VII	60	2
5	21EC0005	Hands on course on Internet of Things	VII	60	2

11:28 AM | 9th BGS Meeting at 11:00am

Zoom Meeting | 11:31 AM | 9th BGS Meeting at 11:00am

Seeking Approval

- R2021 B.E -ECE Syllabi(VII and VIII Semesters)
- R2021(Amendment 2023) B.E -ECE Curriculum and Syllabi
- R2021 Department specific Value Added Skills

11:31 AM | 9th BGS Meeting at 11:00am

ANNEXURE - I

Minutes of 8th Board of Studies Meeting - Action Taken Report

S.NO	Suggestions	Action plan	Reflection in the Curriculum
1	Suggested that, 21EC6706/ Mixed Signal IC Design course the prerequisite can be changed to VLSI design & Digital design instead of VLSI signal Processing	Corrected and implemented	Page No.171
2	Suggested to In the professional elective 21EC5705/ Robotics and Artificial Intelligence course the prerequisite can be changed to Python, IoT or Sensor related course instead of Analog Electronics.	Corrected and implemented	Page No.153
3	Suggested to In theory cum practical course 21EC6602/ Transmission lines and Radiation Systems the practical components are seems to be tougher for the students. So, this can be modified with simulation experiments.	Implemented and modified with simulation experiments.	Page No.136
4	The syllabus framed under skill curriculum should not be replicated with the topics given in any of the professional core, laboratory or professional electives.	Checked and Corrected	

ANNEXURE -II

List of Department Specific Value Added Skill for Approval

S.No	Skill Code	Skill Name	Semester	Contact Periods	Credits
1	21EC2DS01	PCB Design	II	60	2
2	21EC3DS01	Mobile Robotics	III	60	2
3	21EC4DS01	Signal and Image Processing using MATLAB	IV	60	2
4	21EC5DS01	System Verilog with UVM	V	60	2
5	21EC6DS01	Hands on course on Internet of Things	VI	60	2

ANNEXURE -III

List of NPTEL Courses

Course Id	NPTEL Course Name	Duration	Start/ End date
Noc23-ee87	Machine Learning And Deep Learning - Fundamentals And Applications	12 Weeks	July 24,2023 October 13,2023
noc23-ee84	Introduction To Photonics	12 Weeks	July 24,2023 October 13,2023
Noc23-ee142	Analog VLSI Design	12 Weeks	July 24,2023 October 13,2023
noc23-ee136	Simulation Of Communication Systems Using Matlab	12 Weeks	July 24,2023 October 13,2023
noc23-ee119	Pattern Recognition And Application	12 Weeks	July 24,2023 October 13,2023
Noc23-bt38	Fundamentals Of Micro And Nanofabrication	12 Weeks	July 24,2023 October 13,2023
noc23-ee113	Modern Digital Communication Techniques	12 Weeks	July 24,2023 October 13,2023
noc23-ee131	Deep Learning	12 Weeks	July 24,2023 October 13,2023