



FRANCIS XAVIER ENGINEERING COLLEGE

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Department of Electronics & Communication Engineering

Board of Studies Meeting – I

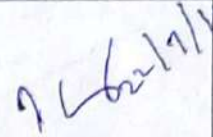
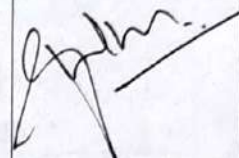

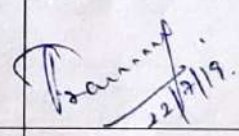

Date : 22.07.2019

Time : 10:00 am

Venue : Presentation Hall

The first Board of Studies Meeting of **Electronics & Communication Engineering Department** was convened under the chairmanship of **Mrs. L.R.Priya**, Professor and Head / **Electronics & Communication Engineering Department**, of Francis Xavier Engineering College in the **Presentation hall** on 22th July, 2019 at 10:00 am.

External Members present :

S. No	Members as per UGC Norms	Members Nominated	Signature
1	Head of the Department concerned (Chairman)	Mrs. L.R.Priya , Head & Asso. Prof, ECE department	
2	One Expert nominated by the Vice Chancellor from a panel of six recommended by the college Principal	Dr.G.Karpagarajesh M.E.,Ph.D.,MISTE Associate Professor, Department of Electronics and Communication Engineering, Government College of Engineering Tirunelveli	
3	Two experts in the subject from outside the college nominated by the Academic Council	Dr.S.Ananth Associate Professor Department of Communication Engineering School Of Electronics Engineering (Sense) VIT-Vellore	
		Dr. P. Sivakumar Professor, Department of Electronics and Communication Engineering, Kalasalingam University, Krishnankoil-626126. Virudhunagar , Tamil Nadu, India.	
4	One representative from industry/corporate sector/allied area relating to placement	Er.Francy Williams Senior Engineer, All India Radio , Palayamkottai, Tirunelveli	
5	One Post Graduate meritorious aluminous nominated by the Principal	Mr.S.J. Jinolin Jacob HR manager, US Operations Insigma Technologies Pvt Ltd Trivandrum	

Internal Members Present :

S. No	Name of the Member	Designation	Signature
1	Dr. G.Rajakumar	Professor/ECE	
2	Dr. N. Muthu Kumaran	Asso. Prof/ECE	
3	Dr.K.Lakshmi Narayanan	Asst. Prof/ECE	
4	Mrs.M.Navaneetha Velammal	Assoc. Professor/ECE	
5	Mr. C.Amarsingh Feroz	Asso. Prof./ECE	
6	Mrs.J.Friska	Asso. Prof./ECE	
7	Mrs.D.Regii Timna	Asst. Prof/ECE	
8	Mr.S.Esakki Rajavel	Asst. Prof/ECE	
9	Mr.R.Kabilan	Asst. Prof/ECE	
10	Mrs.M. Chitra Evangelin	Asst. Prof/ECE	
11	Mr. P. Kannan	Asst. Prof/ECE	
12	Mr.M.Suresh Chinna Thambi	Asst. Prof/ECE	
13	Mr. S.Allwin Devaraj	Asst. Prof/ECE	
14	Mrs.E.Francy Irudaya Rani	Asst. Prof/ECE	
15	Mr.R.Prem Ananth	Asst. Prof/ECE	
16	Mr.G.Jayaraman	Asst. Prof/ECE	
17	Mr.B.Pradheep T Rajan	Asst. Prof/ECE	
18	Mrs.R.Niranjana	Asst. Prof/ECE	
19	Mrs.Shala.F.John	Asst. Prof/English	
20	Mrs.S.Sudharthini	Asst. Prof/Physics	
21	Ms.Jimla Pushpam	Asst. Prof/Chemistry	
22	Mr. Veludhas	Asst. Prof/Maths	

Ex-Officio members present:

S. No	EX OFFICIO MEMBERS		Signature
1	Dr.D.C.Joy winnie Wise	Principal, Francis Xavier Engineering College, TVL	
2	Dr.R.Vedhapriyavadhana, Asso.Prof, ECE Dept	Professor Academics, Francis Xavier Engineering College, Tirunelveli	
3	Dr.M.Caroline Viola Stella Mary, Prof, ECE Dept.	Controller of Examinations, Francis Xavier Engineering College, Tirunelveli	

Minutes of Meeting

The session started with a prayer, by Mrs.J.Friska, Assoc.Prof/ECE who also introduced and welcomed the members of the Board of Studies, which includes a University Nominee, Expert members, an industry member and an alumni of the college.

The Chairman of the board Mrs. L.R.Priya, Professor and Head / **Electronics & Communication** Engineering Department introduced the external members of "Board of Studies". She pointed out that the local need is to strengthen **Communication, Analytical Ability and Industrial Exposure** (Either through industrial projects or attending in-plant training) of our students. Vision, mission, outline about department and its salient features were portrayed by the chairman.

Chairman then outlined the agenda for discussion in the meeting.

Agenda :

Item No.	Particulars
Item : 1.1	Curriculum and Syllabi of B.E Electronics and Communication Engineering
Item : 1.2	Curriculum and Syllabi of M.E Communication Systems
Item : 1.3	Curriculum and Syllabi of M.E VLSI Design
Item : 1.4	Reference & Text Books

- The chairman listed out the courses chosen for each semester under B.E EC programme
- The details about the professional core subjects, professional electives, theory cum laboratory integrated subjects, laboratory courses and the credits for each were highlighted.
- Curriculum for entire course and syllabus for 1st for semesters were discussed.

Dr.G.Karpagarajesh

- Skimmed through the curriculum and **appreciated the overall neat framework of syllabus** which seemed to include all the required courses.
- suggested some **books on electronic devices** by authors namely, **Robert Boylestad, Sedha Smith & Sariko, instead of Millman Hawkins**
- suggested that the topic, **introduction to logic families** be included in VLSI as 1st unit, as knowledge about this topic is necessary for other units in vlsi.
- discussed with **Signals & systems** subject handling staff, and **rearranged a few topics** in the same syllabus.
- suggested that **DTSP from 5th sem can be moved to 4th sem**. This would provide a continuity for Signals & systems course in 3rd semester.
- Suggested to rename the **Engineering Mathematics** subject as Matrices or Calculus, so that it will be clear in marksheet what topics in maths has been learnt.
- suggested that the topic **line coding schemes** be added in 2nd unit of Analog & Digital Communication subject.

Dr.S.Ananth

- pointed out that few topics in 4th unit of **Electronic Devices & Circuits** course might be repetitive namely LCD, LED etc. He discussed and rearranged few topics and gave suggestions for renaming the unit titles.
- **suggested to merge first 3 units of TLWG and first 2 units of AWP as 5 units of a single subject**. Also suggested to give a convenient title for the subject.
- also suggested to make sure that specific topics, namely, waveguides and TE, TM modes be included in **RF & microwave design** subject.
- suggested that the curriculum be made transparent to students.
- recommended that in **Analog electronics lab** frequency response of CE, CS alone be included and CB, CC to be avoided.
- He also congratulated the meticulous efforts taken in framing the syllabus considering the minute details.

Dr.P.Sivakumar

- suggested IC (integrated course) which links the theory and practical sessions. Credits of total 5 or 4 can be allotted for such courses. This would prove to be easier for students to learn practical knowledge and to score better.
- explained about the floating credit system in use in Kalasalingam University. Depending on results, electives for next semester can be chosen by students.
- suggested that Signals & Systems course be introduced as theory cum practical course.
- suggested that DTSP from 5th sem can be moved to 4th sem. This would provide a continuity for Signals & systems course in 3rd semester.

Er.Francy Williams

- enquired about communication syllabus and asked if digital modulation schemes are included or not. The topics included were explicitly showed to her.
- She also suggested that according to anna university norms it must be made compulsory for students to attend inplant training and internships. Practical work area experience is must.

Er.S.J. Jinolin Jacob

- expressed his satisfaction that all necessary basics have been covered in 1st year itself.
- He also suggested that communication skills is very important for students from southern region
- C,C++ course be made compulsory for all, as it is basic requirement for placements in both software and core companies.

All the points were duly noted and the necessary changes incorporated in revised syllabus.

Er. S.J. Jinolin Jacob

- expressed his satisfaction that all necessary basics have been covered in 1st year itself.
- He also suggested that communication skills is very important for students from southern region
- C,C++ course be made compulsory for all, as it is basic requirement for placements in both software and core companies.

Item : 1.2**Curriculum and Syllabi of M.E Communication Systems**

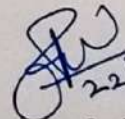
- **Dr.G.KarpagaRajesh and Dr.S.Ananth** insisted that more of system oriented simulation laboratory work be included. They were **persistent about getting required softwares to be installed**, namely, **OptiSim for optical lab and HFSS for antenna design lab is mandatory.**
- **Dr.G.KarpagaRajesh and Dr.S.Ananth** appreciated that **Millimeter wave communication subject is a very good choice.**
- **Dr.S.Ananth** suggested that **5G communication or Near field Communication (NFC) based courses could be included as electives**, as they are upcoming trending technologies.

Item : 1.3**Curriculum and Syllabi of M.E VLSI Design**

- First year Mathematics course name was changed as **Advances in applied Mathematics** by **Dr.G.Karpagarajesh.**
- **PG syllabus formation** and its design flow were accepted by the BOS members.

R. Ananth
22/7/19.

[MEMBER SECRETARY]


22/7/19.

Chairman- Board of Studies

Department of Electronics &
Communication Engineering

ATTACHMENT I : B.E ECE – Curriculum & Syllabi -2019 (revised)

ATTACHMENT II : M.E COMMUNICATION SYSTEMS –

Curriculum & Syllabi -2019

ATTACHMENT III : VLSI DESIGN - Curriculum & Syllabi -2019